

1 1. A cellular telephone comprising:
2 a first processor;
3 a second processor;
4 a first bus coupling said first and second
5 processors; and
6 a device to selectively bypass the first
7 processor.

1 2. The telephone of claim 1 wherein said first
2 processor is an applications processor.

1 3. The telephone of claim 1 including a keypad, said
2 first processor coupled to said keypad to receive keypad
3 inputs.

1 4. The telephone of claim 1 including a display,
2 said first processor coupled to said display to provide
3 outputs to said display.

1 5. The telephone of claim 2 wherein said second
2 processor is a baseband processor.

1 6. The telephone of claim 1 wherein said device
2 selectively bypasses the first processor if the first
3 processor fails to respond.

1 7. The telephone of claim 1 wherein the second
2 processor selectively bypasses the first processor to make
3 an emergency call.

1 8. The telephone of claim 1 wherein said telephone
2 includes a keypad, keypad entries being provided to said
3 first processor, said device selectively shunting said
4 keypad entries to said second processor.

1 9. The telephone of claim 1 including a display,
2 said display coupled to receive outputs from said first
3 processor, said device to selectively bypass the first
4 processor to provide outputs to said display from said
5 second processor.

1 10. The telephone of claim 1 including a display and
2 a keypad, said first processor coupled to said display and
3 said keypad and said second processor coupled to said
4 display and said keypad through said first processor and
5 said device.

1 11. A method comprising:
2 establishing communications between an
3 input/output device and a first processor; and
4 in response to the detection of an event,
5 providing said communications to a second processor.

1 12. The method of claim 11 including selectively
2 coupling keypad entries to a second processor when a first
3 processor fails to respond.

1 13. The method of claim 11 including coupling keypad
2 entries directly to the first processor except when the
3 first processor fails to respond.

1 14. The method of claim 11 including detecting an
2 emergency call and in response to the detection of an
3 emergency call, coupling keypad entries directly to a
4 baseband processor.

1 15. The method of claim 11 wherein detecting an event
2 includes detecting the failure of a first processor to
3 respond.

1 16. The method of claim 15 including detecting the
2 failure of the first processor to respond within a
3 predetermined amount of time.

1 17. The method of claim 11 including coupling said
2 second processor to said first processor and coupling said
3 first processor directly to a keypad and a display.

1 18. The method of claim 17 including selectively
2 coupling said display and said keypad directly to said
3 second processor.

1 19. The method of claim 11 including providing a
2 first processor which acts as an applications processor.

1 20. The method of claim 19 including providing a
2 second processor that acts as a baseband processor.

1 21. An article comprising a medium storing
2 instructions that enable a processor-based system to:
3 establish communications between an input/output
4 device and a first processor; and
5 in response to the detection of an event, provide
6 said communications to a second processor.

1 22. The article of claim 21 further storing
2 instructions that enable the processor-based system to
3 selectively couple keypad entries to a second processor
4 when a first processor fails to respond.

1 23. The article of claim 21 further storing
2 instructions that enable the processor-based system to
3 couple keypad entries directly to the first processor
4 except when the first processor fails to respond.

1 24. The article of claim 21 further storing
2 instructions that enable the processor-based system to
3 detect an emergency call and in response to the detection
4 of an emergency call, couple the keypad entries directly to
5 a baseband processor.

1 25. The article of claim 12 further storing
2 instructions that enable the processor-based system to
3 detect the failure of the first processor to respond.

1 26. The article of claim 25 further storing
2 instructions that enable the processor-based system to
3 detect the failure of the first processor to respond within
4 a predetermined amount of time.

1 27. The article of claim 21 further storing
2 instructions that enable the processor-based system to
3 couple said second processor to said first processor and
4 couple said first processor directly to a keypad and a
5 display.

1 28. The article of claim 27 further storing
2 instructions that enable the processor-based system to
3 selectively couple said display and said keypad directly to
4 said second processor.

1 29. The article of claim 21 further storing
2 instructions that enable the processor-based system to
3 establish communications between an input/output device and
4 a first processor that is an applications processor.

1 30. The article of claim 29 further storing
2 instructions that enable the processor-based system to
3 provide a second processor that acts as a baseband
4 processor.